

**TRAINING & PLACEMENT CELL
COLLEGE OF TECHNOLOGY**

CTE/TPC/4503
Dated: January 19, 2026

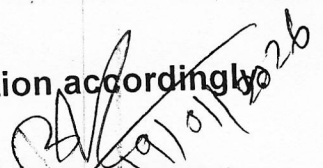
NOTICE

B. Tech & M. Tech ECE, CS, EE and IT (June 2026 Passing)

As per the communication received from **M/s NextlCron Technologies Pvt. Ltd. Bangalore** they are likely to visit our college on **2/3 week February 2026**. They are looking for the B. Tech & M. Tech Final year students of ECE, CS, EE and IT. Final year students of these disciplines who are registered with the Training and Placement Cell of the College and have not been placed so far may confirm their interest in appearing for the campus interview by submitting their resume in the prescribed format in the T & P Cell between **12-01 pm and 3-5 pm on 24th January, 2025**.

- **Eligibility Criteria:** 60% throughout (X, XII and CGPA)
- **Selection Process:** Written Tests and interviews
- **Job Description Attached**

Students are advised to take timely necessary action accordingly


(B. K. Singh)
Officer I/C,
Training & Placement

Copy:

Notice Board NCT/PCT, Mandakni Bhawan & VS Bhawan
All concerned Students through E Mail
Head's ECE, EE, IT or CSE, COT, for information and display on departmental notice boards
Director Placement & Counselling for kind information
Dean CT for kind information
In-charge College/ University Website for uploading

Job Description for Campus Interview at G. B. Pant Univ of Agri and Tech:

Graduate Engineer and Trainee Engineer Positions

Nov 30, 2025

Company Overview: NextICron Technologies specializes in semiconductor IP design and services. Our expertise lies in high-quality semiconductor intellectual property solutions, tailored to meet the needs of our clients in the semiconductor industry. With our innovation-driven approach, we aim to deliver world-class products and services in the field of semiconductors.

We are hiring Graduate Engineers and Trainee Engineers for multiple positions across various domains. Bachelor's and master's students from Electronics and Communication, Electrical, Computer and IT disciplines are welcome to participate. The selection process will be based on the right aptitude and attitude. CTC will be competitive to the industry standards.

Positions Available:

1. Layout [Trainee/Graduate/Associate] Engineer

- Job Responsibilities:
 - Draw physical layouts of SRAM blocks and standard cells.
 - Perform DRC (Design Rule Check), LVS (Layout Versus Schematic), and Extraction.
 - Run IR/EM simulations to verify the layout integrity and reliability.
- Skills Required:
 - Understanding of layout design and verification tools.
 - Basic knowledge of CMOS technology and design principles.

2. Design/Automation [Trainee/Graduate/Associate] Engineer

- Job Responsibilities:
 - Create and simulate circuit schematics for various components and blocks.
 - Run design margin and characterization simulations.
 - Analyze simulation results and optimize designs.
 - Write software automation scripts
 - Develop and maintain automated workflows for the design process.
- Skills Required:
 - Knowledge of analog and digital circuit design.
 - Familiarity with simulation tools and circuit analysis techniques.
 - Proficiency in programming languages such as Python, Perl, or Shell scripting.
 - Understanding of circuit design and simulation processes.

Eligibility:

- Open to 3rd year/Final year bachelor's and final year Master's students from Electronics and Communication, Electrical, Computer, and IT Engineering backgrounds. Minimum GPA 6.0
- Selection based on aptitude (logical reasoning, problem-solving) and attitude (learning mindset, willingness to collaborate).

Selection Process:

1. Written Test:

- Questions related to basic digital electronics, CMOS processing steps, basic gates layout, stick diagram, schematic and truth tables, SRAM operation, and circuits.
- Basic aptitude and programming skills

2. Interview:

- Technical interview to assess the candidate's domain knowledge.
- Evaluation of aptitude, problem-solving skills, and approach to real-world engineering challenges.

If you have the right aptitude, a passion for technology, and the drive to grow in an exciting engineering environment, we would love to meet you!